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Daniel C. Edelstein et al.

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U.S. PATENT DOCUMENTS

*EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
PR	US2002/0172025	11/2002	Megahed et al.			
	US2003/0067052	4/2003	Matsuo et al.			
	US2002/0197844	12/2002	Johnson et al.			
	6,551,931	4/2003	Edelstein et al.			
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	6,333,559	12/2001	Costrini et al.			
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	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
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PR	Research Disclosure, April 2000, AN ON-CHIP THREE-DIMENSIONAL INDUCTOR BY DAMASCENE PROCESS, pages 682-683.
PR	Burghartz et al., MONOLITHIC SPIRAL INDUCTORS FABRICATED USING A VLSI CU-DAMASCENE INTERCONNECT TECHNOLOGY AND LOW-LOSS SUBSTRATES, pages 4.5.1-4.5.4, 1996 IEEE.

EXAMINER John Brah	DATE CONSIDERED 4/29/05
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<i>ED</i>	6,297,140	10/2001	Uzoh et al.			
	6,251,528	6/2001	Uzoh et al.			
	6,187,680	2/2001	Costrini et al.			
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<i>ED</i>		Edelstein et al., SPIRAL AND SOLENOIDAL INDUCTOR STRUCTURES ON SILICON USING CU-DAMASCENE INTERCONNECTS, 1998 IEEE, pages 18-20.
<i>ED</i>		Burghartz et al., SPIRAL INDUCTORS AND TRANSMISSION LINES IN SILICON TECHNOLOGY USING COPPER-DAMASCENE INTERCONNECTS AND LOW-LOSS SUBSTRATES, 1997IEEE, pages 1961-1968.

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John Arab

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